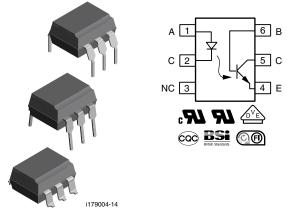
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Vishay Semiconductors

Optocoupler, Phototransistor Output, with Base Connection



DESCRIPTION

The CNY17 is an optically coupled pair consisting of a gallium arsenide infrared emitting diode optically coupled to a silicon NPN phototransitor.

Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The CNY17 can be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CRT modulation.

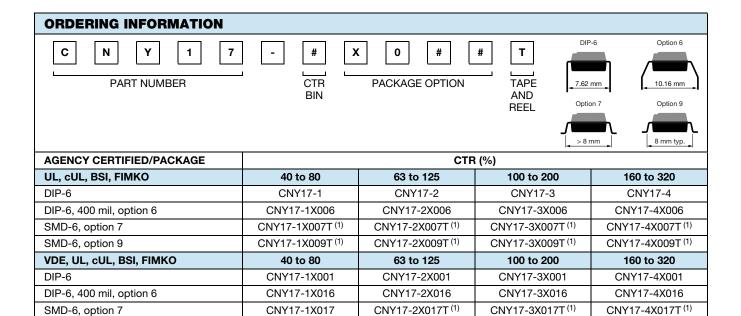
FEATURES

- Isolation test voltage: 5000 V_{RMS}
- · Long term stability
- Industry standard dual-in-line package
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912
 COMPLIANT

AGENCY APPROVALS

Safety application model number covering all products in this datasheet is CNY17. This model number should be used when consulting safety agency documents.

- UL file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5)
- BSI IEC 60950, IEC 60065
- FIMKO EN60950
- CQC GB8898-2011



Note

⁽¹⁾ Also available in tubes, do not put T on the end.

SMD-6, option 9

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CNY17-2X019T⁽¹⁾

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V _R	6	V			
Forward current		I _F	60	mA			
Forward surge current	t _p ≤ 10 µs	I _{FSM}	2.5	А			
LED power dissipation	at 25 °C	P _{diss}	100	mW			
OUTPUT							
Collector emitter breakdown voltage		BV _{CEO}	70	V			
Emitter base breakdown voltage		BV _{EBO}	7	V			
Collector current		Ι _C	50	mA			
	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	Ι _C	100	mA			
Power dissipation		P _{diss}	150	mW			
COUPLER							
Isolation test voltage between emitter and detector	t = 1 min	V _{ISO}	5000	V _{RMS}			
Storage temperature		T _{stg}	-55 to +150	°C			
Operating temperature		T _{amb}	-55 to +110	°C			
Soldering temperature ⁽¹⁾	2 mm from case, \leq 10 s	T _{sld}	260	°C			
Total power dissipation		P _{diss}	250	mW			

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

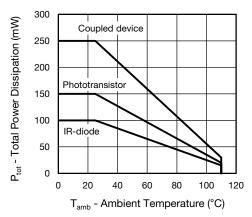


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	I _F = 60 mA		V _F		1.39	1.65	V		
Breakdown voltage	I _R = 10 μΑ		V _{BR}	6			V		
Reverse current	$V_R = 6 V$		I _R		0.01	10	μA		
Capacitance	V _R = 0 V, f = 1 MHz		Co		25		pF		
Thermal resistance			R _{th}		750		K/W		

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CNY17



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
OUTPUT									
Collector emitter capacitance	$V_{CE} = 5 V$, f = 1 MHz		C _{CE}		5.2		pF		
Collector base capacitance	$V_{CE} = 5 V$, f = 1 MHz		C _{CB}		6.5		pF		
Emitter base capacitance	V _{CE} = 5 V, f = 1 MHz		C _{EB}		7.5		pF		
Thermal resistance			R _{th}		500		K/W		
COUPLER									
Collector emitter, saturation voltage	$V_{F} = 10 \text{ mA}, I_{C} = 2.5 \text{ mA}$		V _{CEsat}		0.25	0.4	V		
Coupling capacitance			C _C		0.6		pF		
Collector emitter, leakage current	V _{CE} = 10 V	CNY17-1	I _{CEO}		2	50	nA		
		CNY17-2	I _{CEO}		2	50	nA		
		CNY17-3	I _{CEO}		5	100	nA		
		CNY17-4	I _{CEO}		5	100	nA		

Note

• Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
IC/IF	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}$	CNY17-1	CTR	40		80	%
		CNY17-2	CTR	63		125	%
		CNY17-3	CTR	100		200	%
		CNY17-4	CTR	160		320	%
	V _{CE} = 5 V, I _F = 1 mA	CNY17-1	CTR	13	30		%
		CNY17-2	CTR	22	45		%
		CNY17-3	CTR	34	70		%
		CNY17-4	CTR	56	90		%

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
LINEAR OPERATION	(without saturation)	•					
Turn-on time	I_F = 10 mA, V_{CC} = 5 V, R_L = 75 Ω		t _{on}		3		μs
Rise time	I_F = 10 mA, V_{CC} = 5 V, R_L = 75 Ω		t _r		2		μs
Turn-off time	I_F = 10 mA, V_{CC} = 5 V, R_L = 75 Ω		t _{off}		2.3		μs
Fall time	I_F = 10 mA, V_{CC} = 5 V, R_L = 75 Ω		t _f		2		μs
Cut-off frequency	I_F = 10 mA, V_{CC} = 5 V, R_L = 75 Ω		f _{CO}		110		kHz
SWITCHING OPERA	FION (with saturation)	•					
Turn-on time	I _F = 20 mA	CNY17-1	t _{on}		3		μs
	I _F = 10 mA	CNY17-2	t _{on}		4.2		μs
		CNY17-3	t _{on}		4.2		μs
	I _F = 5 mA	CNY17-4	t _{on}		6		μs
	I _F = 20 mA	CNY17-1	t _r		2		μs
Rise time	I _F = 10 mA	CNY17-2	t _r		3		μs
		CNY17-3	t _r		3		μs
	I _F = 5 mA	CNY17-4	t _r		4.6		μs
	I _F = 20 mA	CNY17-1	t _{off}		18		μs
Turn off times	10	CNY17-2	t _{off}		23		μs
Turn-off time	I _F = 10 mA	CNY17-3	t _{off}		23		μs
	I _F = 5 mA	CNY17-4	t _{off}		25		μs
Fall time	I _F = 20 mA	CNY17-1	t _f		11		μs
	I _F = 10 mA	CNY17-2	t _f		14		μs
		CNY17-3	t _f		14		μs
	I _F = 5 mA	CNY17-4	t _f		15		μs

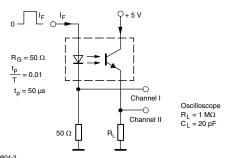
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Document Number: 83606

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Fig. 2 - Test Circuit, Non-Saturated Operation

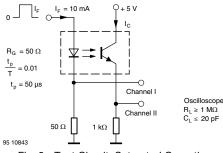
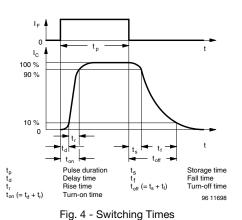


Fig. 3 - Test Circuit, Saturated Operation



SAFETY AND INSULATION RATINGS VALUE UNIT PARAMETER SYMBOL MAXIMUM SAFETY RATINGS Output safety power 700 mW P_{SO} Input safety current 400 mΑ I_{SI} °C 175 Safety temperature T_{SI} Comparative tracking index CTI 175 **INSULATION RATED PARAMETERS** 5000 V_{RMS} Maximum withstanding isolation voltage VISO Maximum transient isolation voltage 8000 VIOTM Vpeak Maximum repetitive peak isolation voltage VIORM 890 V_{peak} $T_{amb} = 25 \text{ °C}, V_{DC} = 500 \text{ V}$ Insulation resistance ≥ 10¹² R_{IO} Ω $T_{amb} = 100 \ ^{\circ}C, V_{DC} = 500 \ V$ ≥ 10¹¹ Isolation resistance RIO Ω Climatic classification (according to IEC 68 part 1) 55/115/21 Environment (pollution degree in accordance to DIN VDE 0109) 2 Standard DIP-4 ≥7 mm Creepage distance SMD ≥7 mm Standard DIP-4 ≥8 mm Clearance distance SMD ≥8 mm DTI Insulation thickness ≥ 0.4 mm

Note

• As per DIN EN 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

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TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

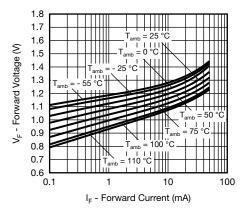


Fig. 5 - Forward Voltage vs. Forward Current

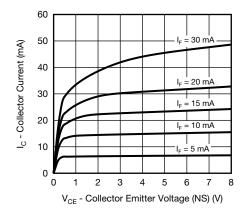


Fig. 6 - Collector Current vs. Collector Emitter Voltage (NS)

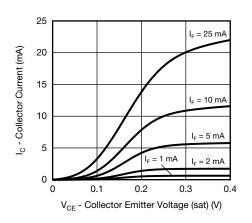


Fig. 7 - Collector Current vs. Collector Emitter Voltage (sat)

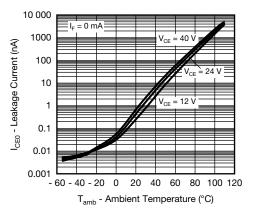


Fig. 8 - Leakage Current vs. Ambient Temperature

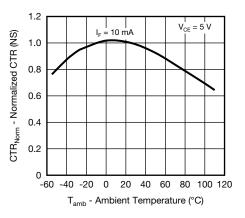


Fig. 9 - Normalized CTR (NS) vs. Ambient Temperature

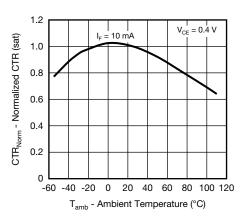


Fig. 10 - Normalized CTR (sat) vs. Ambient Temperature

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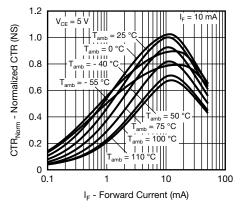


Fig. 11 - Normalized CTR (NS) vs. Forward Current

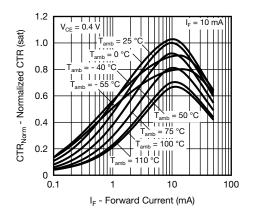


Fig. 12 - Normalized CTR (sat) vs. Forward Current

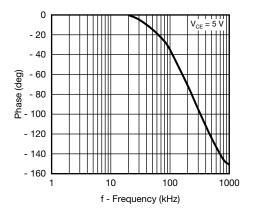


Fig. 13 - CTR Frequency vs. Phase Angle

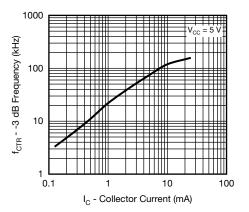


Fig. 14 - CTR -3 dB Frequency vs. Collector Current

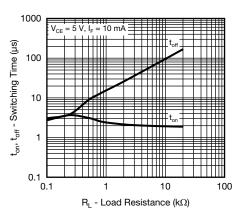


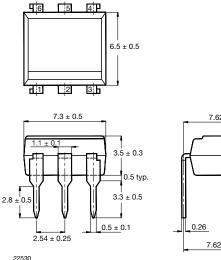
Fig. 15 - Switching Time vs. Load Resistance

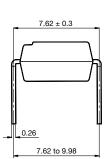
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PACKAGE DIMENSIONS in millimeters

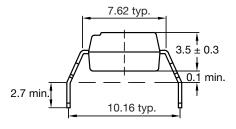


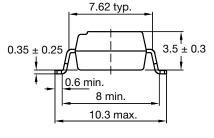


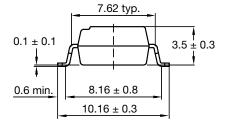
Option 6

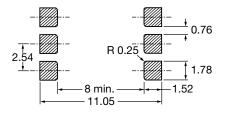
Option 7

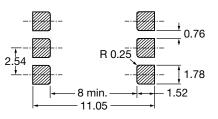
Option 9





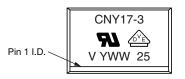






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PACKAGE MARKING

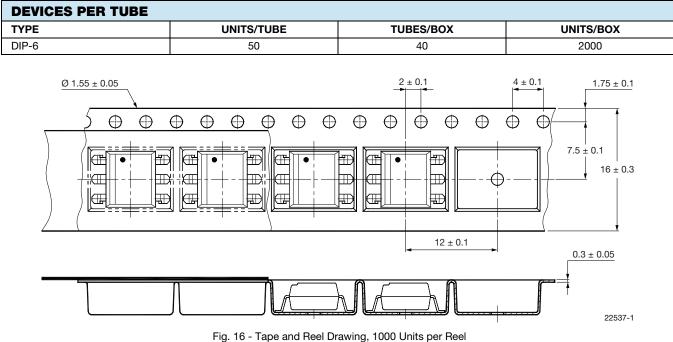


Notes

- VDE logo is only marked on option 1 parts. Option information is not marked on the part.
- Tape and reel suffix (T) is not part of the package marking.



TUBE AND TAPE INFORMATION



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