

## FGH40N60SMD 600 V, 40 A Field Stop IGBT

### Features

- Maximum Junction Temperature : T<sub>J</sub> = 175<sup>o</sup>C
- Positive Temperaure Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage:  $V_{CE(sat)}$  = 1.9 V(Typ.) @ I<sub>C</sub> = 40 A
- High Input Impedance
- Fast Switching: E<sub>OFF</sub> = 6.5 uJ/A
- Tighten Parameter Distribution
- RoHS Compliant

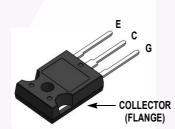
## Applications

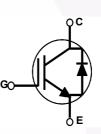
· Solar Inverter, UPS, Welder, PFC, Telecom, ESS

### October 2014

## **General Description**

Using novel field stop IGBT technology, Fairchild's new series of field stop 2<sup>nd</sup> generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.





### **Absolute Maximum Ratings**

Symbol	Description		Ratings	Unit
V <sub>CES</sub>	Collector to Emitter Voltage	600	V	
V <sub>GES</sub>	Gate to Emitter Voltage	± 20	V	
	Transient Gate to Emitter Voltage	± 30	V	
Ic	Collector Current	@ T <sub>C</sub> = 25°C	80	A
'U	Collector Current	@ T <sub>C</sub> = 100 <sup>o</sup> C	40	A
I <sub>CM (1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	120	A
IF	Diode Forward Current	@ T <sub>C</sub> = 25°C	40	A
'F	Diode Forward Current	@ T <sub>C</sub> = 100 <sup>o</sup> C	20	A
I <sub>FM (1)</sub>	Pulsed Diode Maximum Forward Cur	120	A	
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	349	W
. D	Maximum Power Dissipation	@ T <sub>C</sub> = 100 <sup>o</sup> C	174	W
TJ	Operating Junction Temperature		-55 to +175	°C
T <sub>stg</sub>	Storage Temperature Range	-55 to +175	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 secor	nds	300	°C

#### Notes:

1: Repetitive rating: Pulse width limited by max. junction temperature

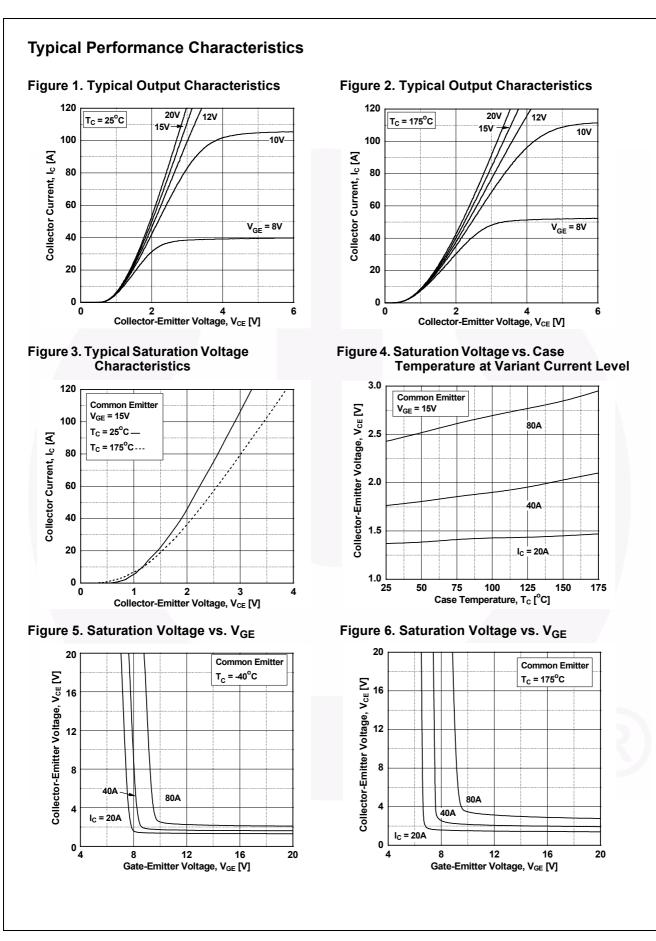
Symbo	I	Par	r		Тур.		Max.		Unit		
R <sub>0JC</sub> (IGBT)	Thern	Thermal Resistance, Junction to Ca				-		0.43		°C/W	
R <sub>0JC</sub> (Diode)	J <sub>JC</sub> (Diode) Thermal Resistance, Junction to Cas			ise		-		1.5		°C/W	
R <sub>θJA</sub>	Thern	nal Resistance, Junct	ion to An	nbient		-		40		°C/W	
Package	e Marki	ng and Order	ing In	form	ation		- I		-		
Part Nu		nber Top Mark Pack			Packing Method	Reel Size		Tape Width		Quantity	
FGH40N60SMD FGH40N60SMD TO-2		247	Tube	N/A		N/A		30			
Electric	al Char	acteristics of	the lo	GBT	T <sub>C</sub> = 25°C unless othe	rwise noted					
Symbol		Parameter		Test Conditions		Min.	Тур.	Max.	Unit		
Off Charact		o Emittor Prockdown	Voltage	V	0 \/  _ = 250 \		600			V	
BV <sub>CES</sub> ∆BV <sub>CES</sub>		o Emitter Breakdown ure Coefficient of Brea		$V_{GE} = 0 V, I_{C} = 250 \mu A$			000	-	-	V	
$\Delta T_J$	Voltage	are obtained in bree		$V_{GE}$ = 0 V, I <sub>C</sub> = 250 $\mu$ A			-	0.6	-	V/ºC	
I <sub>CES</sub>	Collector (	ollector Cut-Off Current			V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V			-	250	μA	
I <sub>GES</sub>	G-E Leakage Current			V <sub>GE</sub> =	V <sub>GES</sub> , V <sub>CE</sub> = 0 V	-	-	± 400	nA		
On Charact	eristics										
V <sub>GE(th)</sub>	G-E Thres	G-E Threshold Voltage			I <sub>C</sub> = 250 μA, V <sub>CE</sub> = V <sub>GE</sub>			4.5	6.0	V	
()				-	A, V <sub>GE</sub> = 15 V		- 1.9		2.5	V	
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage				I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 175°C			2.1	-	V	
Dynamic C	haracteris	tics							1		
C <sub>ies</sub>	Input Cap						-	1880	-	pF	
C <sub>oes</sub>	Output Capacitance			$V_{CE} = 30 V, V_{GE} = 0 V,$			_	180	-	pF	
C <sub>res</sub>	Reverse T	Reverse Transfer Capacitance			_ f = 1 MHz			50	-	pF	
	Charactori	stics								/	
Switching (		Delay Time					· ·	12	16	ns	
t <sub>d(on)</sub> t <sub>r</sub>	Rise Time			-			_	20	28	ns	
t <sub>d(off)</sub>		Delay Time		Voc =	400 V, I <sub>C</sub> = 40 A,		-	92	120	ns	
t <sub>f</sub>	Fall Time	,		$R_G = 6$	Ω, V <sub>GE</sub> = 15 V,		-	13	17	ns	
E <sub>on</sub>		Switching Loss		Inducti	ve Load, $T_C = 25^\circ$	°C	-	0.87	1.30	mJ	
E <sub>off</sub>		Switching Loss						0.26	0.34	mJ	
E <sub>ts</sub>		ching Loss					-	1.13	1.64	mJ	
t <sub>d(on)</sub>		Delay Time					-	15	-	ns	
t <sub>r</sub>	Rise Time						-	22	-	ns	
t <sub>d(off)</sub>	Turn-Off D	elay Time		$V_{CC} = $	400 V, I <sub>C</sub> = 40 A,		-	116	-	ns	
t <sub>f</sub>	Fall Time				R <sub>G</sub> = 6 Ω, V <sub>GE</sub> = 15 V,			16	-	ns	
E <sub>on</sub>	Turn-On S	Switching Loss		Inducti	ve Load, T <sub>C</sub> = 17	5°C	-	0.97	-	mJ	
E <sub>off</sub>	Turn-Off S	Switching Loss		1			-	0.60	-	mJ	
E <sub>ts</sub>	Tatal Curit	ching Loss		1			-	1.57	_	mJ	

## Electrical Characteristics of the IGBT (Continued)

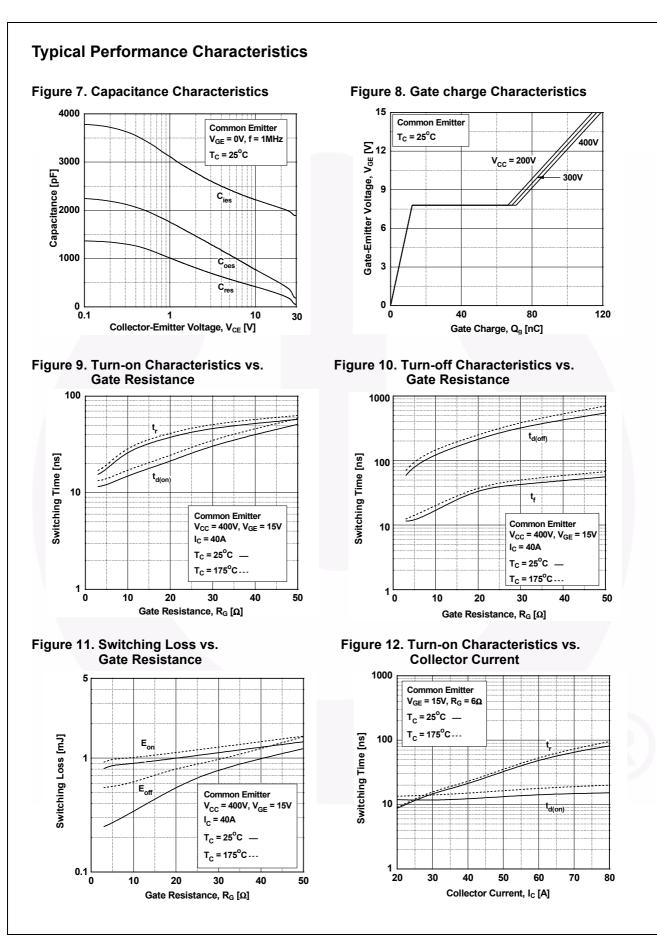
Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
Qg	Total Gate Charge		-	119	180	nC
Q <sub>ge</sub>	Gate to Emitter Charge	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V	-	13	20	nC
Q <sub>gc</sub>	Gate to Collector Charge	VGE - 13 V	-	58	90	nC

Electrical Characteristics of the Diode T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter		Test Condition	on	s	Min.	Тур.	Мах	Unit
V <sub>FM</sub>	Diode Forward Voltage		20 A		T <sub>C</sub> = 25°C	-	2.3	2.8	V
FIN			F 207		T <sub>C</sub> = 175 <sup>o</sup> C	-	1.67	-	
E <sub>rec</sub>	Reverse Recovery Energy				T <sub>C</sub> = 175 <sup>o</sup> C	-	48.9	-	uJ
t.	Diode Reverse Recovery Time		0 A dl_/dt = 200 A/us		T <sub>C</sub> = 25°C	-	36	-	ns
۲r	blode Reverse Receivery fille		$I_F = 20 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}$		T <sub>C</sub> = 175 <sup>o</sup> C	-	110	-	110
Q <sub>rr</sub>	Diode Reverse Recovery Charge				T <sub>C</sub> = 25°C	-	46.8	-	nC
~11	2.000 Hororor (000 for y charge				T <sub>C</sub> = 175 <sup>o</sup> C	-	445	-	

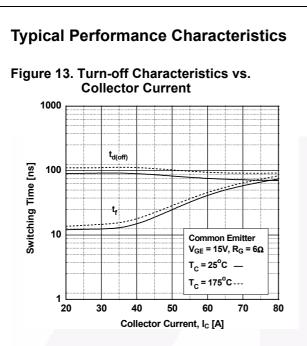


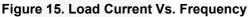
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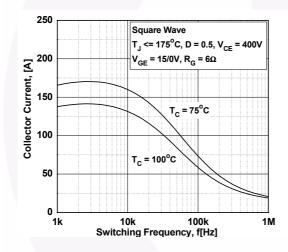
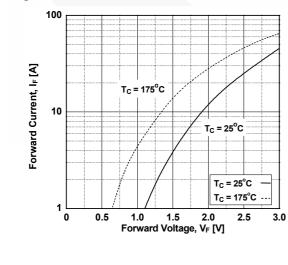
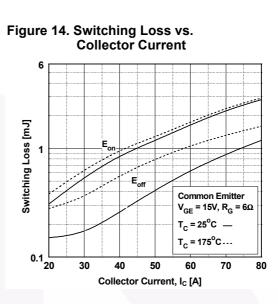


Figure 17. Forward Characteristics





**Figure 16. SOA Characteristics** 

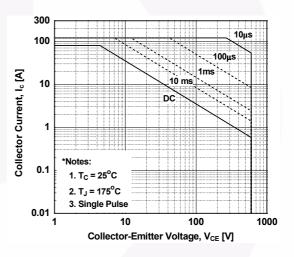
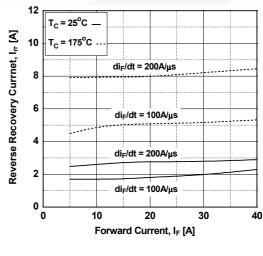
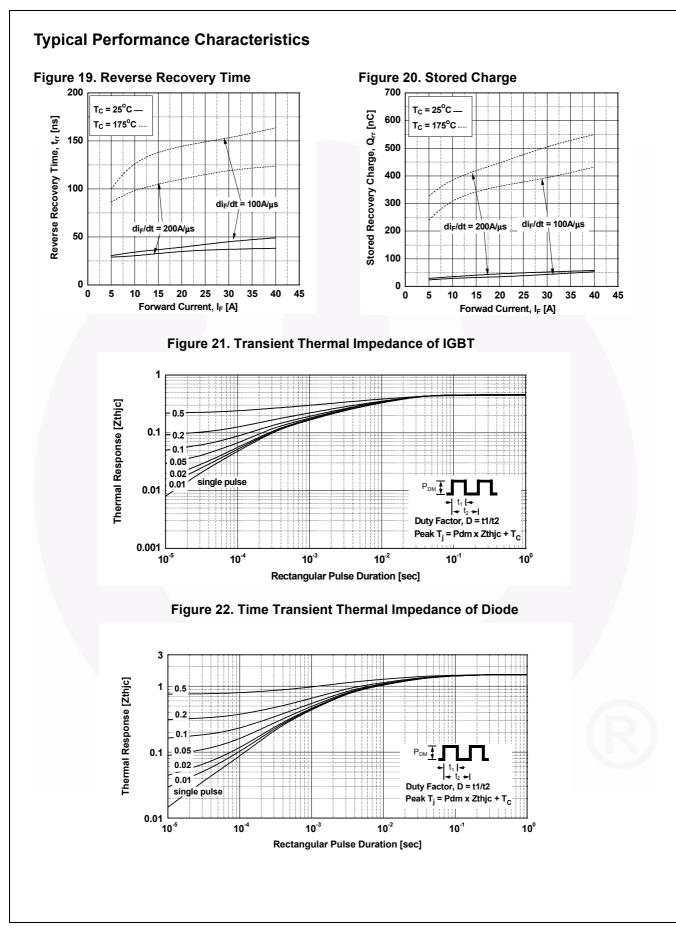
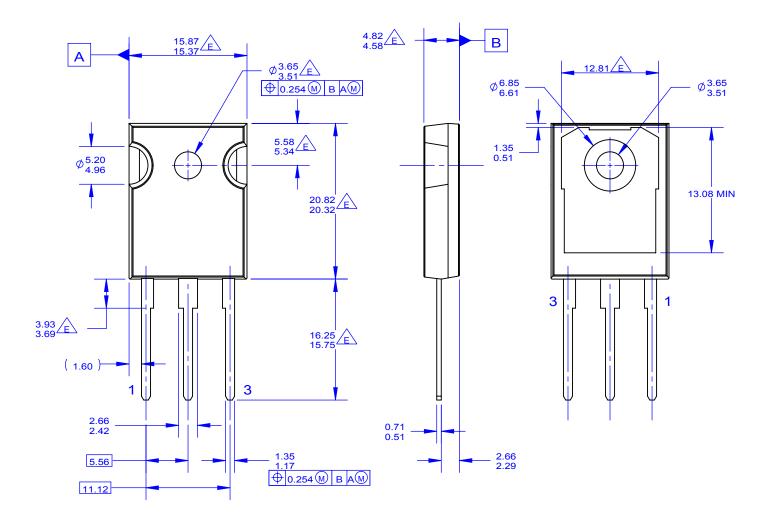


Figure 18. Reverse Recovery Current



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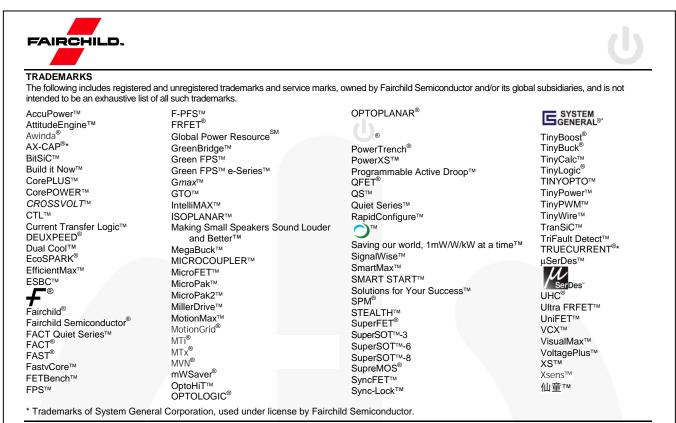




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